

概述

HT75xx-1是一款采用CMOS 技术的低压差线性稳压器。最大输出电流为100mA 且允许的最高输入电压为30V。具有几个固定的输出电压，范围从2.5V 到 5.0V。COMS 技术可确保其具有低压降和低静态电流的特性。

功能特点

- 低功耗
- 低压降
- 较低的温度系数
- 最高输入电压：30V
- 典型静态电流：1.5uA
- 最大输出电流：100mA
- 输出电压精度：±2%
- 封装类型：SOT-23，SOT-89

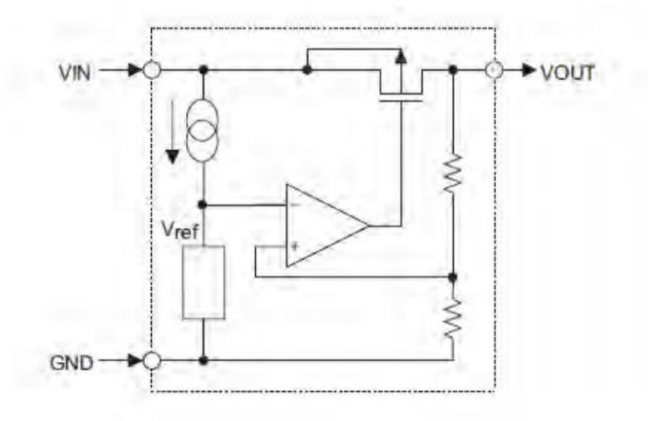
应用领域

- 电池供电设备
- 通信设备
- 音频/视频设备

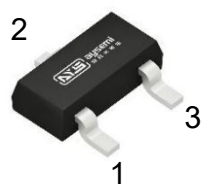
选型表

型号	输出电压	封装类型	最小包装数
HT7525-1	2.5V	SOT-23 SOT-23-3L SOT-89	3000PCS/盘(SOT-23 /SOT-23-3L) 1000PCS/盘(SOT-89)
HT7528-1	2.8V		
HT7530-1	3.0V		
HT7533-1	3.3V		
HT7536-1	3.6V		
HT7540-1	4.0V		
HT7544-1	4.4V		
HT7550-1	5.0V		

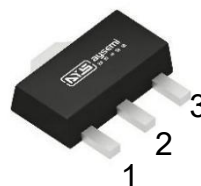
电路功能框图



引脚图



SOT-23/
SOT-23-3L



SOT-89

引脚说明

引脚序号	引脚名称	说明
1	GND	地
2	VIN	输入脚
3	VOUT	输出脚

极限参数

电源供应电压 ----- -0.3V ~+30V 工作环境温度 ----- -40℃~+85℃
 储存温度范围 ----- -45℃~+140℃

注：这里只强调额定功率，超过极限参数所规定的范围将对芯片造成损害，无法预期芯片在上述标示范围外的工作状态，而且若长期在标示范围外的条件下工作，可能影响芯片的可靠性。

热能信息

符号	参数	封装类型	最大值	单位
θ_{JA}	热阻（与环境连接）（假设无环境气流、无散热片）	SOT-23	500	℃/W
		SOT-23-3L	360	℃/W
		SOT-89	200	℃/W
P_D	功耗	SOT-23	0.2	W
		SOT-23-3L	0.2	W
		SOT-89	0.5	W

注： P_D 值是在 $T_a=25^\circ\text{C}$ 时测得。

电气特性

输出型号 HT7525-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	2.425	2.50	2.575	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	30	100	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT} \cdot \Delta V_{IN}}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$,	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A \cdot V_{OUT}}$	$V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 HT7528-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	2.744	2.80	2.856	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	30	100	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT} \cdot \Delta V_{IN}}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN} —		—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A \cdot V_{OUT}}$	$V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 HT7530-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	2.94	3.00	3.06	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	30	100	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 HT7533-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	3.234	3.30	3.366	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	25	55	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 HT7536-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	3.528	3.60	3.672	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	25	55	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 HT7540-1

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	3.92	4.0	4.08	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$ 70		100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	25	55	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^\circ C \leq T_A \leq 85^\circ C$	—	100	—	ppm/ $^\circ C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

输出型号 **HT7544-1**

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	4.312	4.4	4.488	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$	70	100	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 50mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	25	55	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^{\circ}C \leq T_A \leq 85^{\circ}C$	—	100	—	ppm/ $^{\circ}C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

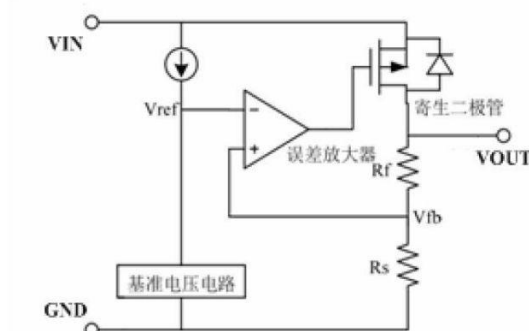
 输出型号 **HT7550-1**

参数说明	符号	测试条件	最小值	典型值	最大值	单位
输出电压	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	4.9	5.0	5.1	V
输出电流	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$	100	150	—	mA
负载调整率	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 70mA$	—	25	60	mV
低压差	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	25	55	mV
静态电流	I_{SS}	无负载	—	1.5	3.0	μA
线性调整率	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \Delta V_{IN}$	$V_{OUT}+1.0V \leq V_{IN} \leq 30V$, $I_{OUT}=1mA$	—	—	0.2	%/V
输入电压	V_{IN}	—	—	—	30	V
温度系数	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^{\circ}C \leq T_A \leq 85^{\circ}C$	—	100	—	ppm/ $^{\circ}C$

注：当 $V_{IN}=V_{OUT}+2.0V$ ，固定负载条件下使输出电压下降2%，此时输入电压和输出电压的差值为低压差值 V_{DIF} 。

功能描述

误差放大器根据反馈电阻 R_s 及 R_f 所构成的分压电阻的输入电压 V_{fb} 同基准电压 V_{ref} 相比较。通过此误差放大器向输出晶体管提供必要的门极电压，而使输出电压不受输入电压或温度变化的影响而保持一定。



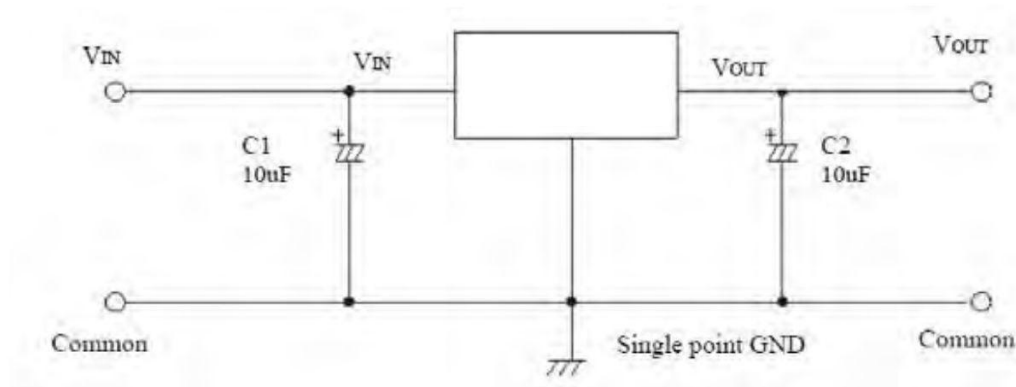
使用注意事项:

- 1) 电路内部使用了相位补偿电路和利用输出电容的ESR 来补偿，所以输出到地一定要接大于2.2uF 的电容器。
- 2) 建议应用时输入和输出使用10uF 有极性电容，并尽量将电容靠近LDO 的 VIN 和 VOUT 脚位。
- 3) 注意输入和输出电压与负载电流的使用条件，避免 IC 内部的功耗(PD)超出封装允许的最大功耗值。

PD 的计算方式: $PD = (V_{IN} - V_{OUT}) \times I_{OUT}$

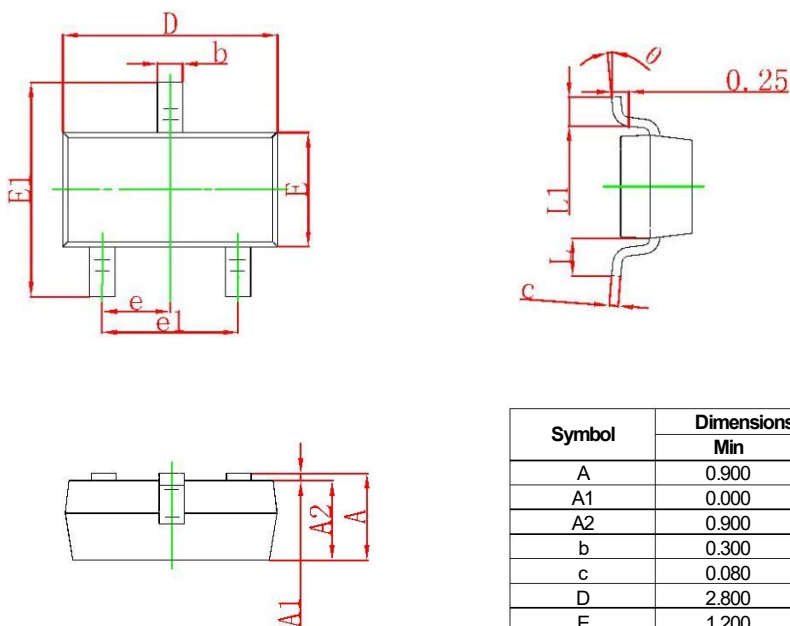
如: HT7550-1, SOT-89 封装, 当 $V_{IN}=12V$, $I_{OUT}=100mA$ 时, 则 $PD = (12-5) \times 100mA = 0.7W$, 超过规格的0.5W, 会损坏IC。不同封装的PD 值, 请参考“热能信息”一栏。

典型应用电路



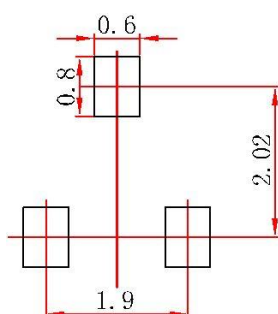
典型应用线路图

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

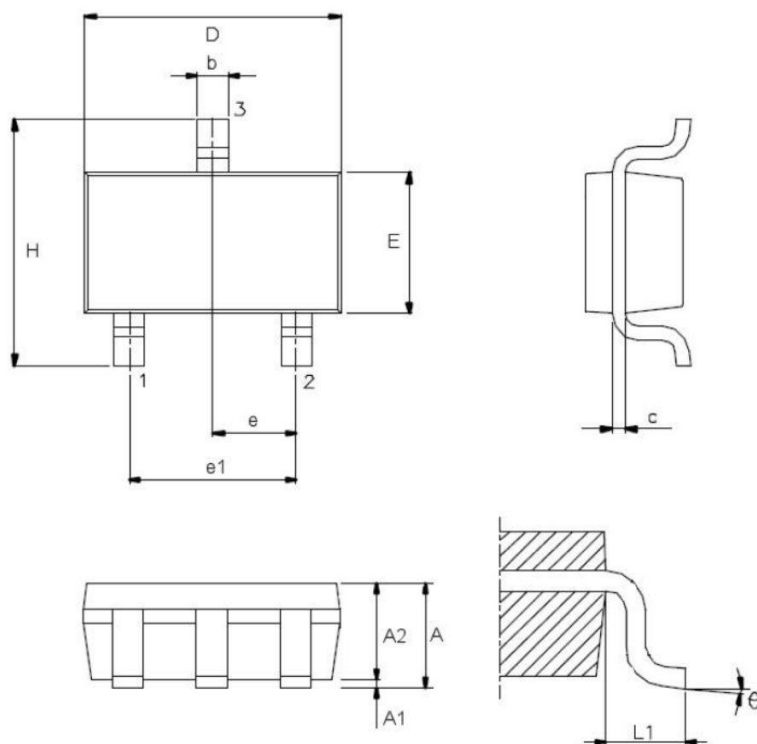
SOT-23 Suggested Pad Layout



Note:

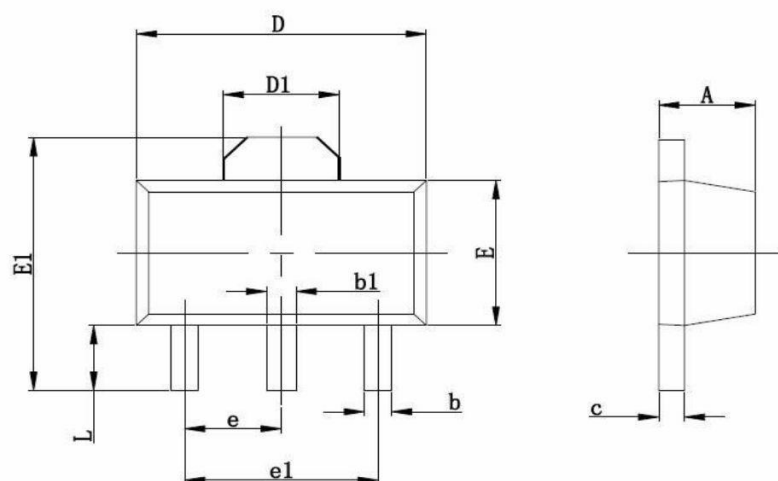
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

SOT-23-3L Package Outline Dimensions



符号	尺寸 (单位: mm)		
	最小	典型	最大
A	—	—	1.45
A1	—	—	0.15
A2	0.90	1.15	1.30
b	0.30	—	0.50
C	0.08	—	0.22
D	—	2.90	—
E	—	1.60	—
e	—	0.95	—
e1	—	1.90	—
H	—	2.80	—
L1	—	0.60	—
θ	0°	—	9°

SOT-89 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.350	0.520	0.013	0.0197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.350	2.550	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060 TYP	
e1	3.000 TYP		0.118 TYP	
L	0.900	1.100	0.035	0.047

Attention

■ Any and all Anyi Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Anyi Semiconductor representative nearest you before using any Anyi Semiconductor products described or contained herein in such applications.

■ Anyi Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in product specifications of any and all Anyi Semiconductor products described or contained herein.

■ Specifications of any and all Anyi Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ Anyi Semiconductor CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all Anyi Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Anyi Semiconductor CO., LTD.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. Anyi Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Anyi Semiconductor product that you intend to use.